



Fifth IEEE International Workshop on Testing Three-Dimensional Stacked Integrated Circuits

3D-TEST



in conjunction with ITC / Test Week 2014

Washington State Convention Center – Seattle, WA, USA

October 23+24, 2014

<http://3dtest.tttc-events.org>

General Chair:

Y. Zorian – Synopsys (US)

Program Chair:

E.J. Marinissen – IMEC (BE)

Finance Chair:

B. Eklow – Cisco Systems (US)

Finance Vice-Chair / Arrangements:

J. Potter – ASSET InterTech (US)

Publicity Chair:

F. von Trapp – 3DInCites (US)

Publication Chair:

L. Ciganda – Politecnico di Torino (IT)

Web Chair:

G. Jervan – Tallinn Univ. of Techn. (EE)

Program Committee Members:

S. Adham – TSMC (CAN)
V. Agrawal – Auburn Univ. (US)
S. Bhatia – Google (US)
K. Chakrabarty – Duke Univ. (US)
S. Chakravarty – Avago Tech (US)
K.Y. Chung – Samsung (KR)
C.J. Clark – Intellitech (US)
E. Cormack – DfT Solutions (UK)
A. Cron – Synopsys (US)
A. Crouch – ASSET InterTech (US)
D. Domke – Texas Instruments (US)
M.-L. Flottes – LIRMM (FR)
P. Franzon – NC State Univ. (US)
S.K. Goel – TSMC (US)
S. Hamdioui – TU Delft (NL)
M. Higgins – Analog Devices (IRL)
C.-L. Hsu – ITRI (TW)
S.-Y. Huang – NTHU (TW)
M. Hutner – Teradyne (CAN)
H. Jun – SK hynix (KR)
S. Kameyama – Fujitsu (JP)
M. Knox – IBM (US)
M. Laisne – Qualcomm (US)
S. Lecomte – Intel (DE)
K.H. Lee – GigaLane (KR)
C.M. Li – NTU (TW)
M. Loranger – FormFactor (US)
A. Majumdar – Xilinx (US)
T.M. Mak – GlobalFoundries (US)
T. McLaurin – ARM (US)
B. Nadeau-Dostie – Mentor Graph. (US)
C. Papameletis – Cadence (US)
B. Patti – Tezzaron Semiconductor (US)
M. Ricchetti – AMD (US)
S. Shaikh – Broadcom (US)
T. Thürigen – Cascade Microtech (DE)
P. Vivet – CEA-Leti (FR)
M. Wahl – Univ. Siegen (DE)
Q. Xu – Chinese Univ. Hong Kong (HK)

Table-Top Demos and Corporate Support

The 3D-TEST Workshop focuses exclusively on test of and design-for-test for three-dimensional stacked ICs (3D-SICs), including Systems-in-Package (SiP), Package-on-Package (PoP), and especially 3D-SICs based on Through-Silicon Vias (TSVs). While 3D-SICs offer many attractive advantages with respect to heterogeneous integration, smaller form-factor, higher bandwidth and performance, and lower power dissipation, there are many open issues with respect to testing such products. The 3D-TEST Workshop offers a forum to present and discuss these challenges and (emerging) solutions among researchers and practitioners alike.

3D-TEST will take place in conjunction with the IEEE International Test Conference (ITC) and is sponsored by the Test Technology Technical Council (TTTC) of the IEEE Computer Society.

Table-Top Demos – The 3D-TEST Workshop offers the opportunity to present Table-Top Demos during the workshop. Table-Top Demo participants get a table and electricity outlet provided by the workshop. A Table-Top Demo presentation may include displaying slides or demoing tools. Typical content may be comprised of technical descriptions, case studies, best practices, and user testimonials of products or solutions. These presentations will be listed in the workshop program booklet along with the regular paper sessions, and should be targeted to the workshop's technical audience. Table-Top Demos differ from other workshop presentations in that company names, logos, and product names may be mentioned explicitly. Proposal selection is based on technical content and relevance to 3D-TEST audience and topics. Table-Top Demo tables will be assigned in a first-come-first-served order, but priority will be given to Corporate Supporters (see below). If you are interested in presenting a Table-Top Demo, please contact the workshop's Program Chair Erik Jan Marinissen at <erik.jan.marinissen@imec.be>

Corporate Support – Companies are invited to provide financial support to the 3D-TEST Workshop. In return, the supporting corporations will be recognized by the workshop in various ways, including: display corporate logo on workshop's website, program booklet, projection screen, Electronic Workshop Digest, etc. Corporate supporters get priority in the assignment of available Table-Top Demos. For additional details, please contact the workshop's Finance Chair Bill Eklow at <beklow@cisco.com>.

Key Dates

- Submission deadline : **September 17, 2014 - extended**
- Notification of acceptance : **September 22, 2014**
- Camera-ready material : **October 6, 2014**

Further Information

Yervant Zorian – *General Chair*
Synopsys
700 East Middlefield Road
Mountain View, CA 94043-4033, USA
Tel.: +1 (650) 584-7120
E-mail: yervant.zorian@synopsys.com

Erik Jan Marinissen – *Program Chair*
IMEC
Kapeldreef 75
B-3001 Leuven, Belgium
Tel.: +32 (0)16 28-8755
E-mail: erik.jan.marinissen@imec.be

