Curriculum Vitae

Aleksander Sudnitsõn (Alexander Sudnitson)

Date of birth*:* June 14th

Position: associate professor, Department of Computer Engineering of Tallinn University of Technology

Educational history:
1. 1983, Ph.D. in "A Decomposition Method for PLD Synthesis", Tallinn Technical University
2. 1978-1982 postgraduate student at the Institute of Technical Cybernetics of Belorussia.
3. 1967-1972 Tallinn University of Technology, graduated Dipl. Eng. in specialty "Electrical engineering".

Research and professional experience, institutional affiliation and postdoctoral research training:
1. 1995 TTU, associate professor
2. 1992 TTU, senior researcher
3. 1988 TTU, associate professor
4. 1987 Digital Systems Laboratory of Helsinki University of Technology, Finland, guest researcher
5. 1983 TTU, senior lecturer

Current research program:
1. Low power design
2. Combinational and sequential logic synthesis
3. Asynchronous design
4. Computer aided design of digital systems

Grant fundings:
1. 2002-2005, Estonian Science Foundation grant 5643
2. 2001-2002, Estonian Science Foundation grant 4876
3. Aug. 1999, TEMPUS individual grant IMG-98-2007
4. Apr. 1997, TEMPUS individual grant IMG-96-EE-2020
5. Oct.-Nov. 1995, TEMPUS individual grant IMG-95-EE-1025