

FPGA Device

Family: Spartan3E
Device: XC3S500E
Package: FG320
Speed: -4

Spartan-3E Pin Information

Table 1: Slide Switches

Switch	SW3	SW2	SW1	SW0
FPGA Pin	N17	H18	L14	L13

Table 2: Push Button Switches

Push Button	BTN_NORTH	BTN_WEST	BTN_SOUTH	BTN_EAST
FPGA Pin	V4	D18	K17	H13

Table 3: LEDs

LED	LD7	LD6	LD5	LD4	LD3	LD2	LD1	LD0
FPGA Pin	F9	E9	D11	C11	F11	E11	E12	F12

Table 4: On-board 50MHz Clock Oscillator

Clock Source	On-board 50MHz Clock Oscillator
FPGA Pin	C9

Table 5: VGA Display Port

VGA Display Port	Red	Green	Blue	Horizontal Sync	Vertical Sync
FPGA Pin	H14	H15	G15	F15	F14

Table 6: PS/2 Mouse/Keyboard Port

PS/2 Port	Data	Clock
FPGA Pin	G13	G14